

ABSTRACT

In general, the present invention provides an RF signal amplification system having an improved layout. The size of the MMIC can be reduced without loss of functionality and/or additional functionality can be added to the MMIC without increasing the size of the MMIC. The MMIC is configured with an off-chip bias feed system. The MMIC is configured with landing zones for receiving a bond wire such that on-chip bias circuitry can be reduced and/or eliminated.